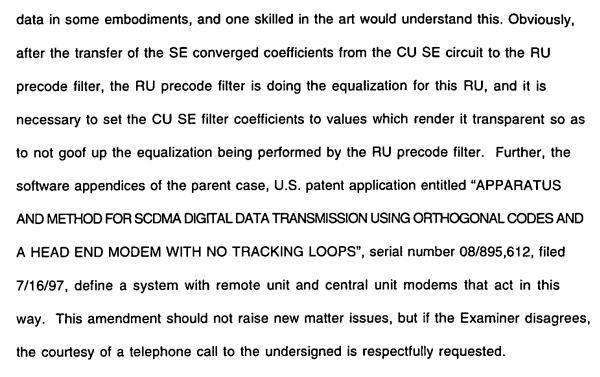


14. The process of claim 11 wherein the step of convolutionally encoding is characterized by parity check polynomials given in octal form as follows: h3=04, h2=10, h1=06, h0=23, d^2_free=5.0, Nfree=1.68, and wherein the nonlinear term is given by D^2[y0(S).AND.D^(-1)y0(D)].

REMARKS

The amendment to page 143, line 26 is made to make clear that which would be apparent to one skilled in the art as inherently necessary in an upstream training process where tap coefficients of a central unit modem are trained and later sent down to the RU transmitter to be used there to calculate new RU precoder filter tap coefficients so as to predistort the transmitted signal so that it will arrive already equalized. Since step 1126 of Figure 53B teaches setting the coefficients of the central unit modem symbol equalizer circuit to one after transferring the converged coefficients to the remote unit transmitter, one skilled in the art would understand that the central unit modem is not equalizing, so the remote unit must be doing the equalizing for its particular signal path for that is the reason for the transfer of the converged coefficients back down to the RU. Therefore, it would be necessary during the convergence process for the RU transmitter to not predistort the equalization training



The same comments apply to the amendment to page 144, line 17 with the additional comment that one skilled in the art of equalization in distributed digital data transmission systems would realize that it was an error to say that all the taps of the FFE and DFE equalization filters are set to one after convergence and transfer of the converged tap coefficients to the RU since this is an obvious error. One skilled in the art would realize that only the main tap of the FFE is set to one and the side taps of the FFE and DFE are set to zero to receieve payload data.

The amendment at page 144, line 8 conforms the description of step 1124 in the upstream equalization process embodiment of Figure 53B for the CDMA specific transmitters disclosed herein to step 1514 of the process of Figure 60 which is an equalization process which is useful in any distributed digital data system with multiple transmitters transmitting to a singe central unit transmitter over different paths regardless of the type of multiplexing in use. Those skilled in the equalization art would

realize that the original description of step 1124 was erroneous in not mentioning convolving the old coefficients with the new coefficients. Further, the software appendices of the parent case, U.S. patent application entitled "APPARATUS AND METHOD FOR SCDMA DIGITAL DATA TRANSMISSION USING ORTHOGONAL CODES AND A HEAD END MODEM WITH NO TRACKING LOOPS", serial number 08/895,612, filed 7/16/97, define a system with remote unit and central unit modems that act in this way. This amendment should not raise new matter issues, but if the Examiner disagrees, the courtesy of a telephone call to the undersigned is respectfully requested.

The change to page 145, lines 10-13 is made to correct an error that persons skilled in the art of equalization would have readily understood was made in the description of how the new CE equalization circuit coefficients are calculated after convergence of the SE coefficients. Persons skilled in the art would appreciate that the new RU receiver SE coefficients cannot be loaded directly into the RU CE equalizer circuit but must, instead, be convolved with the old CE circuit coefficients to generate the new CE coefficients. Further, this amendment conforms the description of the process of Figure 53C to the process described in Figure 60 and the accompanying text. Further, the software appendices of the parent case, U.S. patent application entitled "APPARATUS AND METHOD FOR SCDMA DIGITAL DATA TRANSMISSION USING ORTHOGONAL CODES AND A HEAD END MODEM WITH NO TRACKING LOOPS", serial number 08/895,612, filed 7/16/97, define a system with remote unit and central unit modems that act in this way. This amendment should not raise new matter issues, but if the Examiner disagrees, the courtesy of a telephone call to the undersigned is respectfully requested.

The same comments made regarding the change to page 143, line 26 apply to the



change made to page 170, line 14 since a person skilled in the art would realize that after the CU SE filter coefficients have converged and its coefficients have been sent to the RU to generate new precode filter coefficients there by convolving with the old coefficients of the precode filter, it is necessary to set the SE coefficients in the CU receiver to values such that the CU SE does not screw up the equalization now being performed by the RU precode filter. Those skilled in this art know that those tap coefficients are one for the SE FFE main tap and zero for the SE FFE and DFE side taps. No new matter is believed to be raised by this amendment.

The changes to pages 169 and 170 simply correct duplicate reference numbers which refer to different process steps.

The change to page 170, line 14 simply corrects an error which would have been detected by persons skilled in the art of equalization. After the coefficients of the SE circuit have converged and have been convolved with the old CE coefficients to derive new CE coefficients, the SE coefficients must be set to main tap = 1 and side taps = 0 since to not do so would result in the equalization being done in the RU precode filter in the case of upstream transmissions or the RU CE circuit in the case of downstream transmissions being screwed up by the SE circuit in the RU. Persons skilled in the art appreciate that after the new precode or CE coefficients have been set, the SE coefficients need to be set to a transparent state of main tap = 1 and side taps = 0 so that the SE circuit is





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transparent (and can start to reconverge on subsequent iterations or periodic updates of the precode or CE coefficients).

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Respectfully submitted,

Ronald Craig Fish Reg. No. 28,843 Tel 408 778 3624 FAX 408 776 0426

Ronfpatents@worldnet.att.net

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on 1/2/2001

(Date of Deposit)

Ronald Craig Fish, President

Ronald Craig Fish, a Law Corporation

Reg. No. 28,843